

ABSTRACT OF THE DISCLOSURE

The present invention is directed to a circuit that may be used to protect other circuits from Electrostatic Discharge ("ESD"). A circuit of the present invention comprises a transmission line that couples a source with a circuit that is being protected. A transistor, with an emitter, a collector, and a base, is coupled in reverse mode between an electrical ground and the circuit being protected. This circuit may be used in applications using high frequencies, such as 1 GHz or higher.

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